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This book is based on the premise that starting with a high level programming language has its shortcomings. This premise leads to Patt/Patel's "bottom-up approach" found in "Introduction To Computing." This text covers (in order) a switch level

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abstraction of a MOS

Transistor, Logic

Gates, latches, logic structures

(MUX, Decoder, Adder, gated

latches), finally culminating in an
implementation of memory.

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that I wrote with Professor Sanjay
Jeram Patel of Illinois. The book is
intended as an introduction to
computing for serious students of
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engineering, electrical

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engineering, or any other branch
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